

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of

**JC05 Rec'd PCT/PTO 03 OCT 2005**

Amina Hamidi et al.

Group Art Unit:

Application No.:

Examiner:

Filed: October 3, 2005

Confirmation No.:

For: INSULATED POWER  
SEMICONDUCTOR MODULE WITH  
REDUCED PARTIAL DISCHARGE  
AND MANUFACTURING METHOD**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the above-identified patent application as follows: